

IEC 61508-conformant software development with SPARK

Peter Amey



Agenda

- Introductions
- Some thoughts on standards
- Static analysis
- Unambiguous languages
- Formal methods
- Putting it all together



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Praxis HIS

- specialists in the engineering of high-integrity and safety-critical, software-intensive systems
- delivers services by:
 - provision of tools such as the SPARK Examiner
 - outsourcing complete projects
 - capability enhancement
 - providing key consultancy
- 100+ technical staff and growing
- autonomous but part of the Altran engineering consultancy group
- founded specifically to put engineering into software engineering
 - first software house in the world to achieve ISO 9001 certification



Peter Amey

- Chief Technical Officer at Praxis HIS
- prime author of SPARK Examiner
 - 1992 onwards with Program validation Ltd
 - with Praxis since 1995
- worked on wide range of critical systems projects
 - avionics
 - transport
 - security
- aeronautical engineer, served in Royal Air Force
- worked on armament software safety and certification at (what was then the) A&AEE, Boscombe Down
 - (UK equivalent of Erprobungsstelle 61 at Manching)



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A confession

- I am not very interested in 61508
 - despite being asked here to talk about it
- I am not very interested in DO-178B
 - despite being on the committee currently revising and updating it
- I am not very interested in Def Stan 00-55
 - despite helping write it
- Here is a standard I would sign up to:





The fitness for purpose of a software program shall be established by logical reasoning

Something that met this standard would meet all of the others as well

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Development for Different SILs

IEC 6 1508

	SIL1	SIL2	SIL3	SIL4
Specification	Informal	Informal	Semi-	Formal
			Formal	
Prototyping	R	R	R	R
Coding	HLL	HLL	Safe-	Safe-
	Preferred		Subset	Subset
			HLL	HLL
Defensive Code	-	R	HR	HR
Static Analysis	R	HR	HR	HR
Formal Proof	-	R	R	HR
Dynamic	R	HR	HR	HR
Testing				
Performance	R	R	HR	HR
Testing				



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What about actually doing it?

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7.9 Software verification

• • •

7.9.2.12 Code verification: the source code shall be verified by static methods to ensure conformance to the specified design of the software module (see 7.4.5), the required coding standards (see 7.4.4), and the requirements of safety planning (see 7.3).



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Disadvantages of Dynamic Testing

- Practical disadvantages
 - takes place late in development
 - hard to diagnose unexpected behaviour
 - frequently a bottleneck (e.g. shared use of test rig)
 - very expensive
 - significant source of project risk
- Theoretical limitations
 - high levels of confidence require mathematically infeasible amounts of testing



Theoretical Limitations of Testing

for ultra-high reliability (<10⁻⁷ failures per hour)

- Bayesian mathematics definitively limits what we can claim from statistical testing
- Reliability growth models cannot provide necessary assurance
- Proofs:
 - Butler & Finelli. 1993 (see references)
 - Littlewood & Strigini. 1993 (see references)



Advantages and Disadvantages of Analysis

- Advantages
 - can be used early in the development process
 - can establish properties that cannot be demonstrated in any other way. e.g.
 - proof of absence of run-time errors
 - freedom from timing deadlocks
- Disadvantages
 - can only compare artefacts (e.g. code against specification)
 - what can be achieved is limited by precision of descriptions and notations used



Static analysis - the catch

- We need analysis that is:
 - sound
 - all errors of a particular kind found
 - low false alarm rate
 - fast
 - usable early in development
- What we can achieve depends on the properties of the language we are analysing
- Analysis of general purpose languages cannot achieve these goals



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7.4.4.3 To the extent required by the safety integrity level, the programming language selected

shall:

• • •

b) be completely and unambiguously defined or restricted to unambiguously defined features;

. . .

d) contain features that facilitate the detection of programming mistakes; and

. . .



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Unambiguous source code?

- But: all commonly used languages allow the construction of programs of uncertain meaning
 - ambiguities
- Most also have uncheckable rules
 insecurities



Causes of Uncertainty

- Deficiencies in language definitions
 - Semantics of C integer division
 - Pascal named vs. structural type equivalence
- Implementation freedoms
 - term/expression evaluation order
 - parameter-passing mechanisms



Example of an Ambiguity

$$z := F(x) + G(y);$$

Suppose function **F** modifies **y** as a side-effect of its operation. In this case the meaning of the expression depends on whether **F** or **G** is evaluated first.

A rule stating "functions are not permitted to have side effects" turns the **ambiguity** into an **insecurity**: it does not solve the problem



A Simple C Ambiguity

i = v[i++];

Page 46 of the C++ Annotated Reference Manual states that this leads to the value of *i* being undefined.



A Simple Ada Ambiguity

```
procedure Init2(X, Y : out Integer)
is
begin
   X := 1;
   Y := 2;
end Init2;
What is the meaning of:
```

Init2(A, A);





Resolution

- Ambiguities are resolved by compiler authors
- Insecurities are left for the user to discover
- Possible solutions
 - Invent new languages without these problems
 - Work with dialects associated with compilers
 - Use logically coherent language subsets to overcome ambiguities and insecurities



Safe Subsets

- Potentially give us the best of both worlds:
 - logical soundness and predictability
 - access to standard compilers, tools, training, staff

But

- Level of integrity achievable depends on foundation language chosen
- Subsetting alone may not be enough for the highest integrity levels



Constructing a Safe Subset

- Selection of base language
- Removal of the most troublesome language features
- Limitations on the way remaining features may be used
- Introduction of annotations to provide extra information



The Subset Spectrum

- We can construct subsets that vary on 4 axes:
 - Precision (security and lack of ambiguity)
 - Expressive power
 - Depth of analysis possible
 - Efficiency of analysis process



The Subset Spectrum (contd.)

- Trade-offs quite complex; we are trying to avoid surprise: unexpected behaviour which we don't find until test
 - removing problematic features may reduce this risk
 - increased precision may require reduction in expressive power but improves depth of analysis
 - we may be able to combine expressiveness with depth of analysis but at the cost of efficiency of analysis



The Subset Spectrum (contd.)

- Fundamental trade-off is between discipline we accept to reduce bug insertion and the effort we are prepared to make in bug detection
- For example:
 - unrestricted C provides little or protection from bug insertion
 - Ada requires extra discipline (e.g. strong typing) which reduces bug insertion rate
- A qualitative shift in what is possible only occurs when precision becomes exact



SPARK

- A sub-language of Ada with particular properties that make it ideally suited to the most critical of applications:
 - Completely unambiguous
 - Free from implementation dependencies
 - All rule violations are detectable
 - Formally defined
 - Tool supported
- SPARK subsets of both Ada 83 and Ada 95 are defined
- Language definition is open and widely available



"... one could communicate with these machines in any language provided it was an exact language ..."

"... the system should resemble normal mathematical procedure closely, but at the same time should be as unambiguous as possible."

Alan Turing, 1947

(lecture to the London Mathematical Society on the "Automatic Computing Engine")



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Constructing a Safe Subset - SPARK

- Selection of base language
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Constructing a Safe Subset – SPARK

- Selection of base language
 - ANSI/MIL-STD-1815A-1983 & ISO-8652:1995
- Removal of the most troublesome language features
 - e.g. unrestricted tasking
- Limitations on the way remaining features may be used
 - e.g. limitations on placement of exit and return
- Introduction of annotations to provide extra information
 - core (e.g. Global) and proof (e.g. Post) annotations



Why Annotations?

- Annotations strengthen specifications
 - providing design-by-contract facilities
- Allows analysis without access to procedurebodies
 - which can be done early during development
 - before programs are complete or compilable
- Erroneous constructs are efficiently detected



An example (detection of erroneous constructs)

procedure Inc (X : in out Integer);
--# global in out Callcount;

detection of function side-effect function AddOne (X : Integer) return Integer is XLocal : Integer := X; begin Inc (Xlocal); return XLocal; end AddOne; detection of aliasing Inc (CallCount);



SPARK supports static verification

- These methods can ensure:
 - freedom from language misuse
 - freedom from data and information flow errors
 - freedom from run-time errors
 - specified safety properties are guaranteed
- Source is robust and contains few errors
- Source can be:
 - directly host-tested
 - directly cross-compiled to target
 - used to generate alternate language



Example: run-time error proof

```
type T is range 0 .. 100;
procedure Inc (X : in out T)
--# derives X from X;
is
begin
        X := X + 1;
end Inc;
```

```
Unsimplified verification condition
procedure_inc_1.
H1: true .
H2: x >= t__first .
H3: x <= t__last .
->
C1: x + 1 >= t__first .
C2: x + 1 <= t__last .</pre>
```



Example: run-time error proof

```
type T is range 0 .. 100;
procedure Inc (X : in out T)
--# derives X from X;
is
begin
        X := X + 1;
end Inc;
```

Simplified verification condition





Solutions

```
type T is range 0 ... 100;
                                          Defensive
procedure Inc (X : in out T)
                                         programming
--\# derives X from X;
is
begin
   if X < T'Last then
      X := X + 1;
   end if;
end Inc;
                    type T is range 0 .. 100;
                    procedure Inc (X : in out T)
                    --\# derives X from X;
Logical guard
                    --\# pre X < T'Last;
                    is
                    begin
                       X := X + 1;
                    end Inc;
```



SPARK Results - C130J - Lockheed

- "Very few errors have been found in the software during even the most rigorous levels of FAA testing, which is being successfully conducted for less than a fifth of the normal cost in industry"
- "This level A system was developed at half of typical cost of non-critical systems"
- Productivity gains: X4 on C130J compared to previous safety-critical projects, X16 on C27J with re-use and increased process maturity
- SPARK code was found to have only 10% of the residual errors of full Ada and Ada was found to have only 10% of the residual errors of C



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IEC 61508: Software Detailed Design

- 0: none
- 1: structured methodology (CORE, JSD, MASCOT, Yourdon)
- 2: + semi-formal methods
 (function block diagrams,
 data-flow diagrams, pseudo code)
- 3: + formal methods (VDM, Z, CCS, CSP, HOL, OBJ, LOTOS, Petri nets, state transition diagrams)
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What about actually doing it?

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Formal Methods

- Rumours of the death of Formal Methods are much exaggerated:
 - we successfully use them
 - our customers successfully use them
 - they fit perfectly with SPARK and Correctness by Construction
- Only mathematically-based approaches offer the promise of bug prevention rather than bug detection



Some evidence

CDIS	VDM, CCS, C	10 year warranty, exceptionally trouble free
C130J	CORE, SPARK	Dramatic rise in quality and productivity
SHOLIS	Z, SPARK	00-55, "proof more cost- effective than testing"
MULTOS CA	Z, Ada, C++, SQL, SPARK	ITSEC E6. Formal spec adopted by customer
TCAS	RSML	Formal spec. is "official". Informal spec. abandoned
HDLC	FormalCheck	Serious error found, model checking adopted



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Correctness by Construction

- Formal specs, unambiguous languages and static analysis all help improve code quality and reliability
- If we use all of them and:
 - focus on bug prevention
 - use techniques that find bugs early
 - regard final testing as demonstration of correct behaviour rather than method of finding bugs

then a wonderful synergy occurs:

- we get higher quality at lower cost
- we achieve Correctness by Construction



Low defects at high productivity rates

- Typical defect rate in industry is > 5 defects per KLOC ۲
- Typical productivity rate in industry is < 10 LOC per day
- Sample Praxis rates (for deployed, certified code, including all lifecycle phases and management overhead):

Year	Project	Integrity	Size	Defect/ ksloc	loc/day
1992	ATC display	SIL2	197,000	0.75	13
1997	Helicopter landing system	SIL 4	27,000	0.22	7
1999	Smart card security	ITSEC E6	100,000	0.04	29
2002	Aircraft test set	SIL 0	35,000	<0.1	28
2003	Secure biometrics	CC EAL 5+	10,000	0.00	38



Industry-beating Software Defect Rates





How? The Cost of Not Finding Errors





Metrics (from smart card project) eliminating errors immediately after introduction







The fitness for purpose of a software program shall be established by logical reasoning

DONE! And it is wholly 61508 compliant as a free bonus!

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"Real life problems are those that remain after you have systematically failed to apply all the known solutions"

Edsger Dijkstra, 1973



Any questions?





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